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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	AZEVEDO et al.	Examiner:	Unassigned
Serial No.:	10/807,075	Group Art Unit:	2184
Filed:	March 23, 2004	Docket No.:	SJO920030061US1 (IBMS.072PA)
Title:	METHOD, APPARATUS AND PROGRAM STORAGE DEVICE FOR PROVIDING SELF-QUIESCED LOGIC TO HANDLE AN ERROR RECOVERY INSTRUCTION		

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited in the United States Postal Service, as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on June 25, 2004.

By: Kathleen McDevitt
Kathleen McDevitt

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
Office of Initial Patent Examination's
Filing Receipt Corrections
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed is a photocopy of the filing receipt from the United States Patent and Trademark Office in the above-identified application showing requested corrections. The filing receipt is erroneous in the following respects as reflected in the papers originally filed. The correct name of the first listed applicant is Michael J. Azevedo. Also, the correct title is "Method, Apparatus and Program Storage Device for Providing Self-Quiesced Logic to Handle an Error Recovery Instruction".

Please correct the filing receipt to show the correct name of the first listed applicant and the correct title for the published patent application.

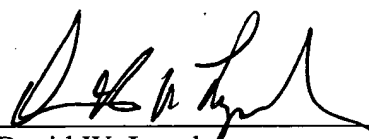
Correction of the records of the United States Patent and Trademark Office and issuance of a corrected filing receipt are respectfully solicited.

Respectfully submitted,

CRAWFORD MAUNU PLLC
1270 Northland Drive
Suite 390
St. Paul, MN 55120
(651) 686-6633

Dated: June 24, 2004

By: _____


David W. Lynch
Reg. No. 36,204

IBMS.072P2



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/807,075	03/23/2004	2184	1344	SJO920030061US1	4	28	8

Crawford Maunu PLLC
 Suite 390
 1270 Northland Drive
 St Paul, MN 55120



CONFIRMATION NO. 2651

FILING RECEIPT



OC000000012524631

Date Mailed: 05/04/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s) *Michael J. Azevedo*
~~J. Azevedo-Michael~~, San Jose, CA;
 , Hugh W. McDevitt, San Jose, CA;
 , Carol Spanel, San Jose, CA;
 , Andrew D. Walls, San Jose, CA;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 05/04/2004

Projected Publication Date: 09/29/2005

Non-Publication Request: No

Early Publication Request: No

Title

Method, apparatus and program storage device for providing self-quiesced logic to handle ~~and an~~ error recovery instruction

Preliminary Class

714

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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